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Customer No.: 31561  
Docket No.: 9289-US-PA  
Application No.: 10/065,342

**In the Claims**

Please amend the claims as follows.

1. (currently amended) A memory module testing/repairing method for testing and repairing a memory module, comprising the steps of:
  - testing the memory module;
  - registering any faulty memory addresses in the memory module; and
  - blocking out fixed address paths to the faulty memory addresses and replacing the faulty memory addresses by selecting standby addresses.
2. (currently amended) The memory module testing/repairing method of claim 1, wherein the step of blocking out fixed address paths to the faulty memory addresses and replacing with specially selected standby addresses is carried out by blowing an electrical fuse.
3. (currently amended) The memory module testing/repairing method of claim 1, wherein the step of testing the memory module includes writing data into [[each]] a memory address and reading data from the memory address and confirming the validity of the read-out data.
4. (currently amended) The memory module testing/repairing method of claim 1, wherein the method further includes setting [[the]] a memory chip on the memory module into a testing mode.
5. (currently amended) A memory module testing/repairing device for testing and repairing a memory module, comprising:

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a storage medium holding testing/repairing programs for testing and repairing the memory module; and

a main computer board having memory module slots therein for accommodating memory modules and retrieving testing/repairing programs from the storage medium before carrying out ~~[[the]]~~ a memory module testing/repairing procedure,

wherein the testing/repairing procedure further includes the following steps:

testing the memory module;

registering ~~[[any]]~~ faulty memory addresses in the memory module;

setting ~~[[the]]~~ a memory chip on the memory module to a testing mode;

blocking out ~~the fixed address~~ ~~[[path]]~~ paths to faulty memory addresses and replacing the faulty memory addresses by ~~selecting~~ standby addresses; and

setting the memory chip on the memory module back to a normal operation mode.

6. (original) The memory module testing/repairing device of claim 5, wherein the storage medium is a system having a floppy disk and a floppy disk reader.

7. (currently amended) The memory module testing/repairing device of claim 5, wherein the storage medium is a system having a hard drive-system.

8. (original) The memory module testing/repairing device of claim 5, wherein the storage medium is a system having an optical disk and an optical disk reader.

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9. (original) The memory module testing/repairing device of claim 5, wherein the device further includes a display monitor for displaying testing/repairing results.